

10/018179

ISSUE FEE IN FILE

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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE



6667513

APPL NUM 10018179	FILING DATE 05/03/2002	CLASS 257	SUBCLASS 1000+	GAU 2811	EXAMINER H. H. H.
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****APPLICANTS:** Skotnicki Thomas; Gvozdecki Romain;

****CONTINUING DATA VERIFIED:** H?

THIS APPLICATION IS A 371 OF PCT/FR00/1537 06/05/2000

YES

****FOREIGN APPLICATIONS VERIFIED:** H?

FRANCE 5907391 06/11/1999

YES

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO 5310-03900
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Foreign priority claimed
35 USC 115 nonclaim cost
Verify and Acknowledged Examiner's initials

TITLE: Semiconductor device with compensated threshold voltage and method for making same

11/H03 Filed Drawings (2 sheets) 5/6/02

NOTICE OF ALLOWANCE MAILED		HOAI PHAM		CLAIMS ALLOWED	
09/08/03		Assistant Examiner		Total Claims 20	Print Claims for 0.0 1
ISSUE FEE		Date Paid		DRAWING	
Amount Due 1300	10-603	Primary Examiner		Sheets Drawn 2	Print Fig. 1
<input type="checkbox"/> TERMINAL		PREPARED FOR ISSUE		Application Examiner	
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